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FIG. 1

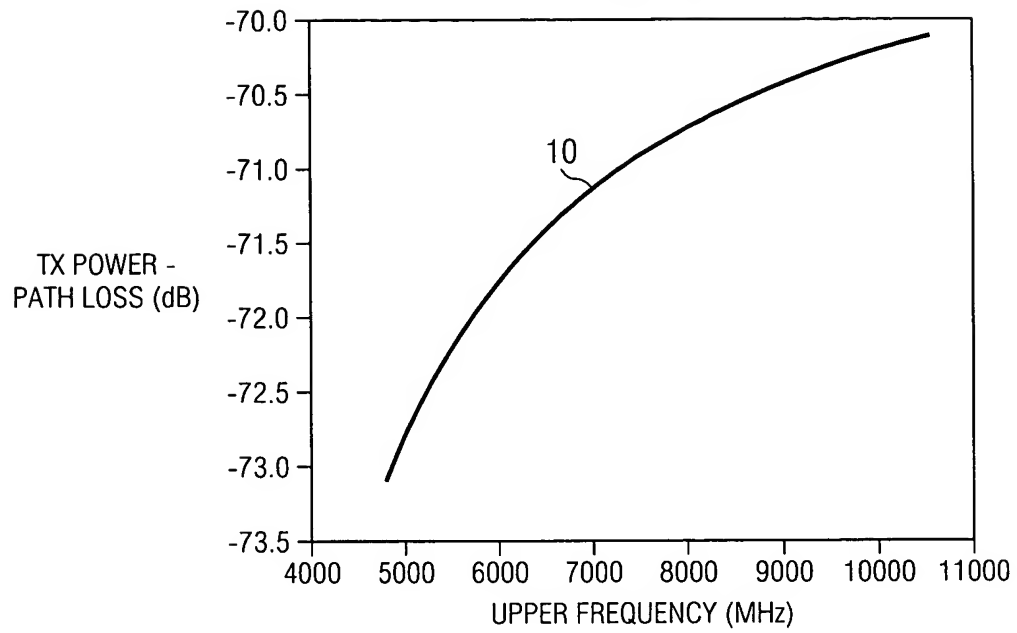


FIG. 2

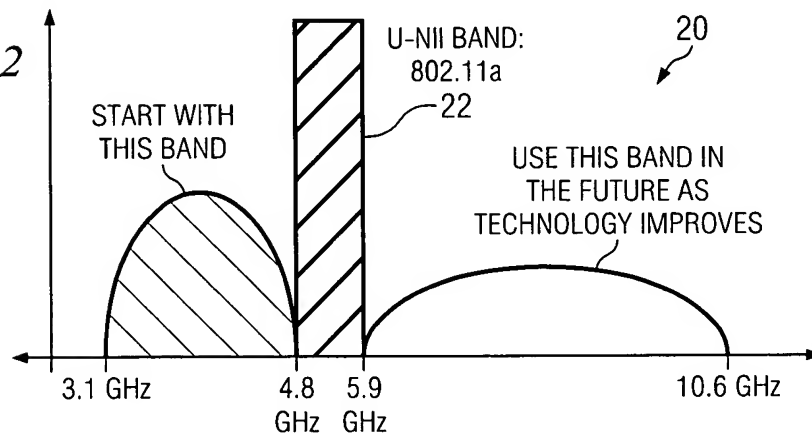
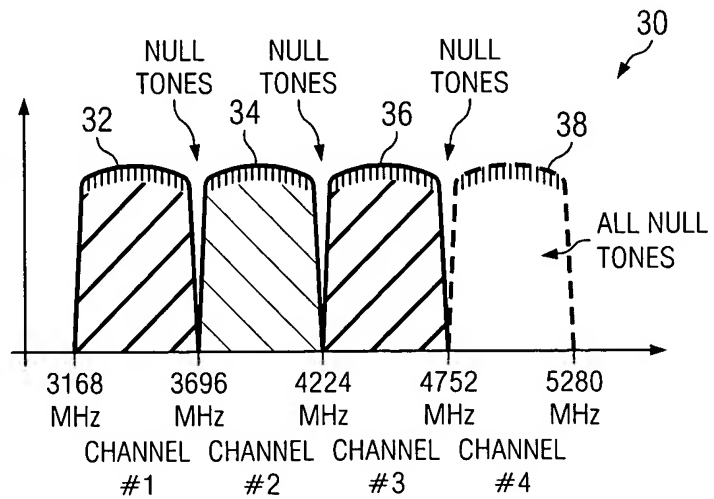
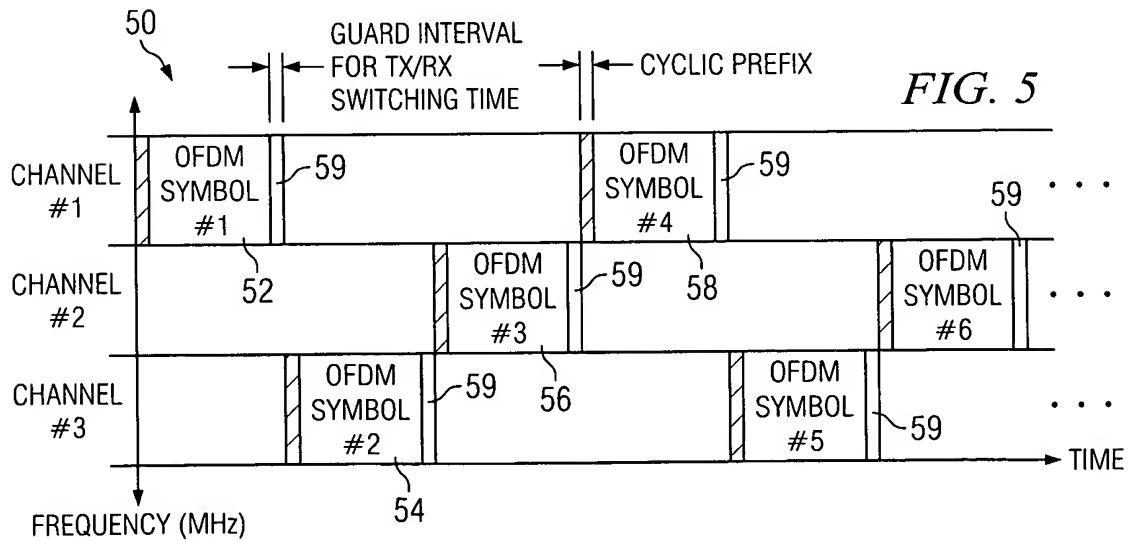
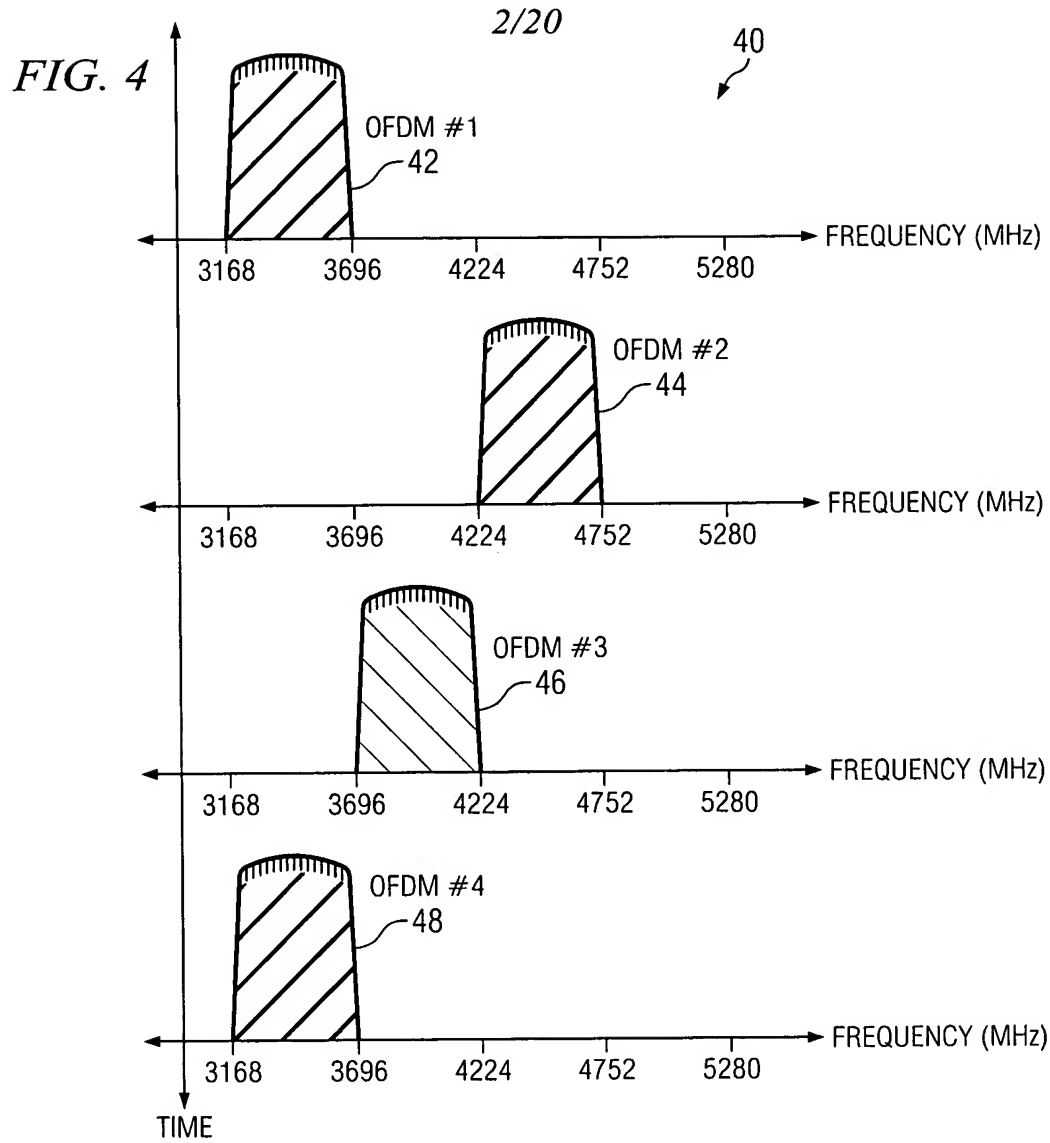


FIG. 3





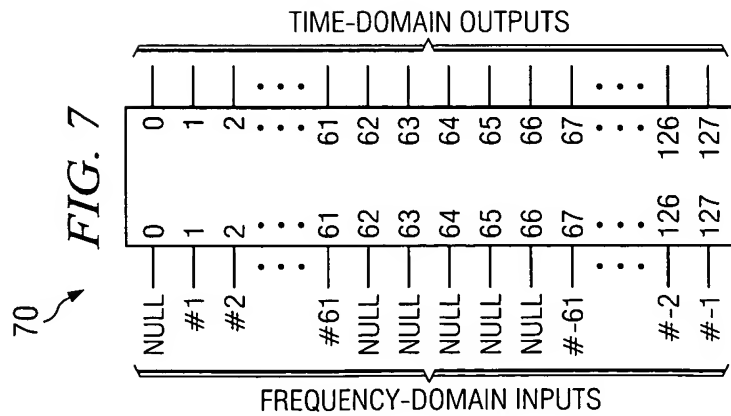
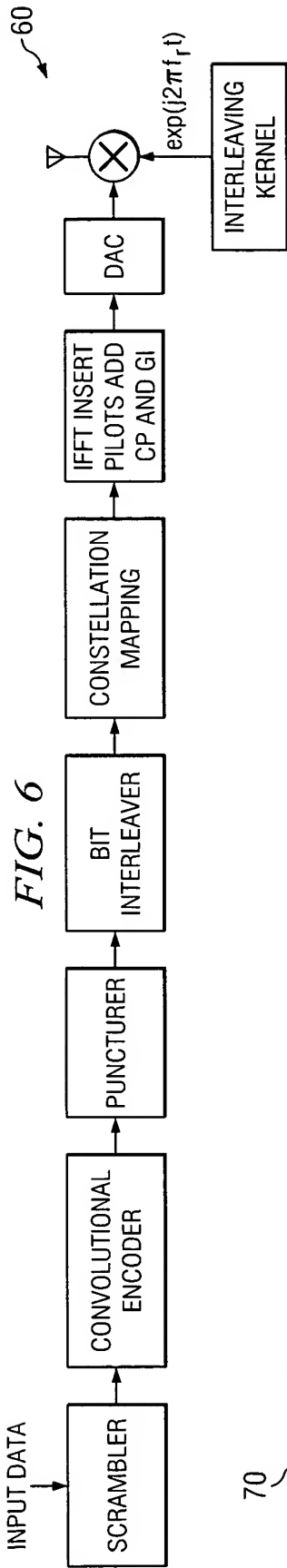
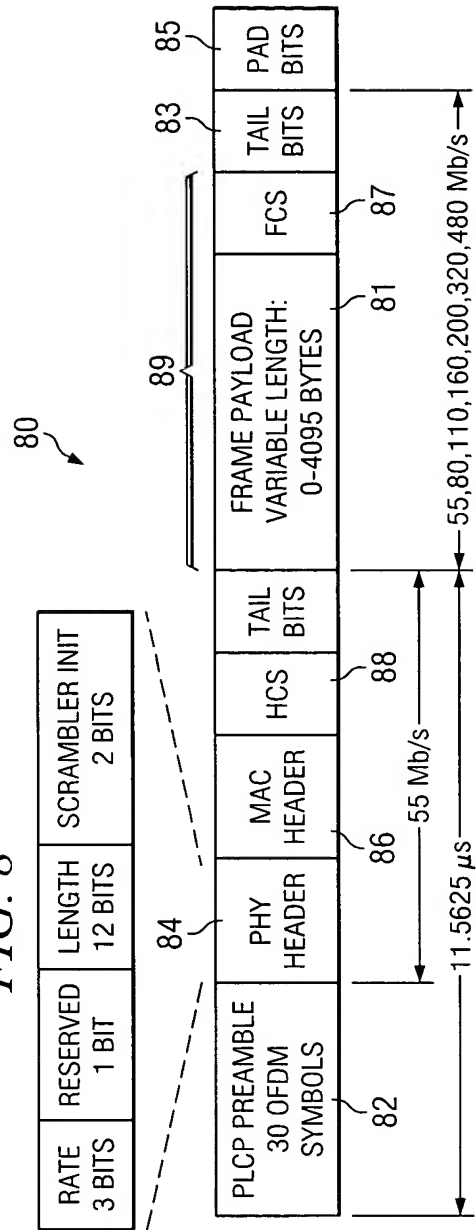


FIG. 8



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FIG. 9

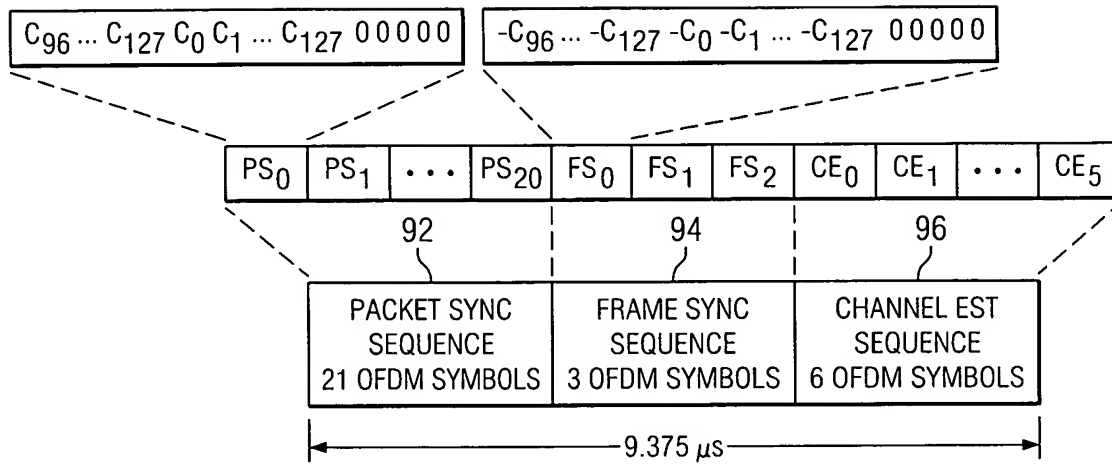


FIG. 10

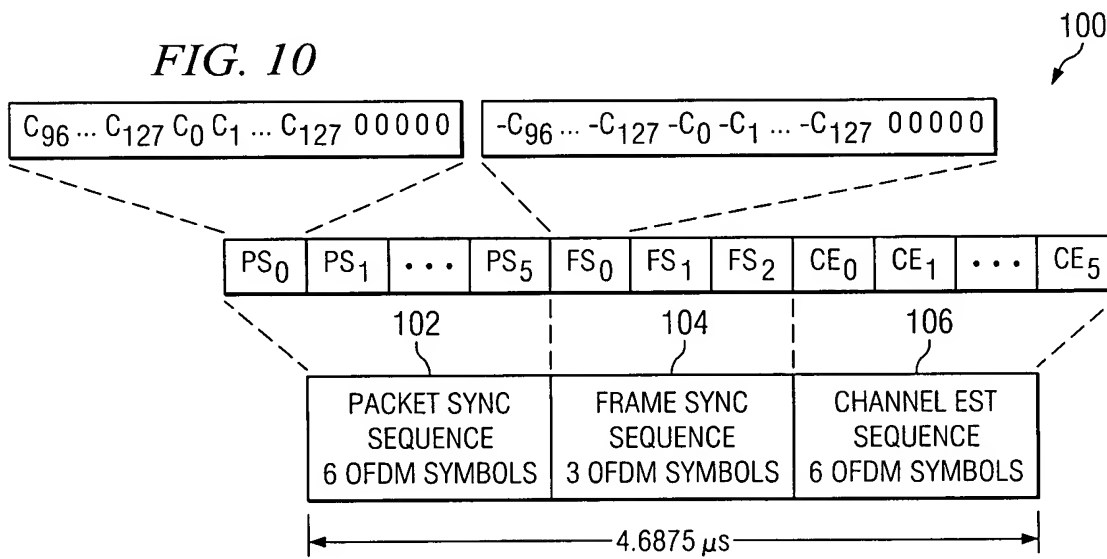
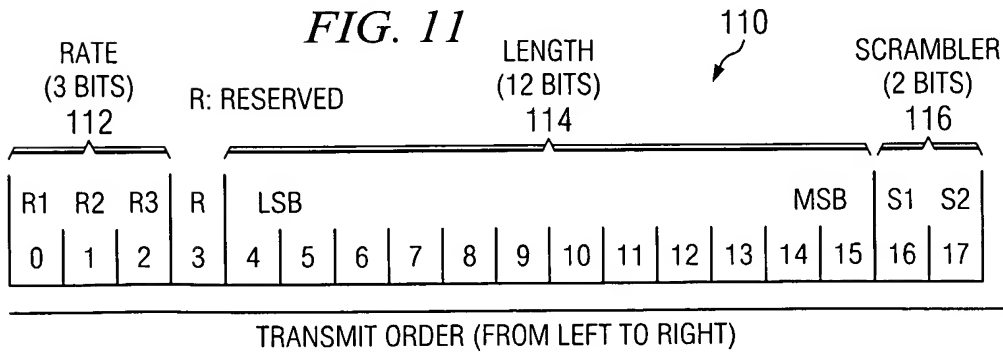
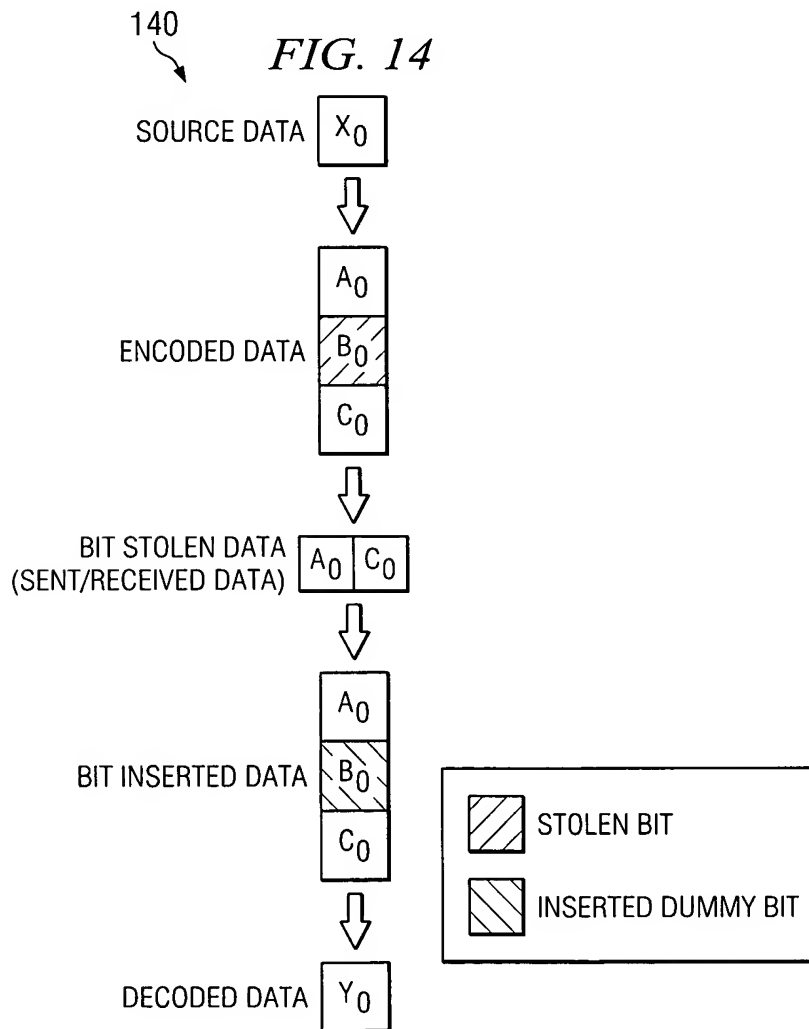
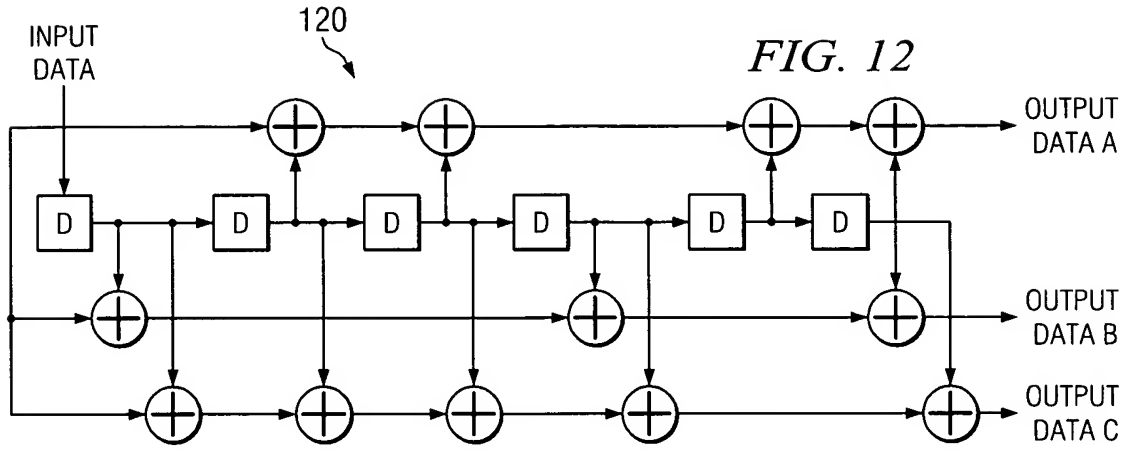


FIG. 11





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FIG. 13

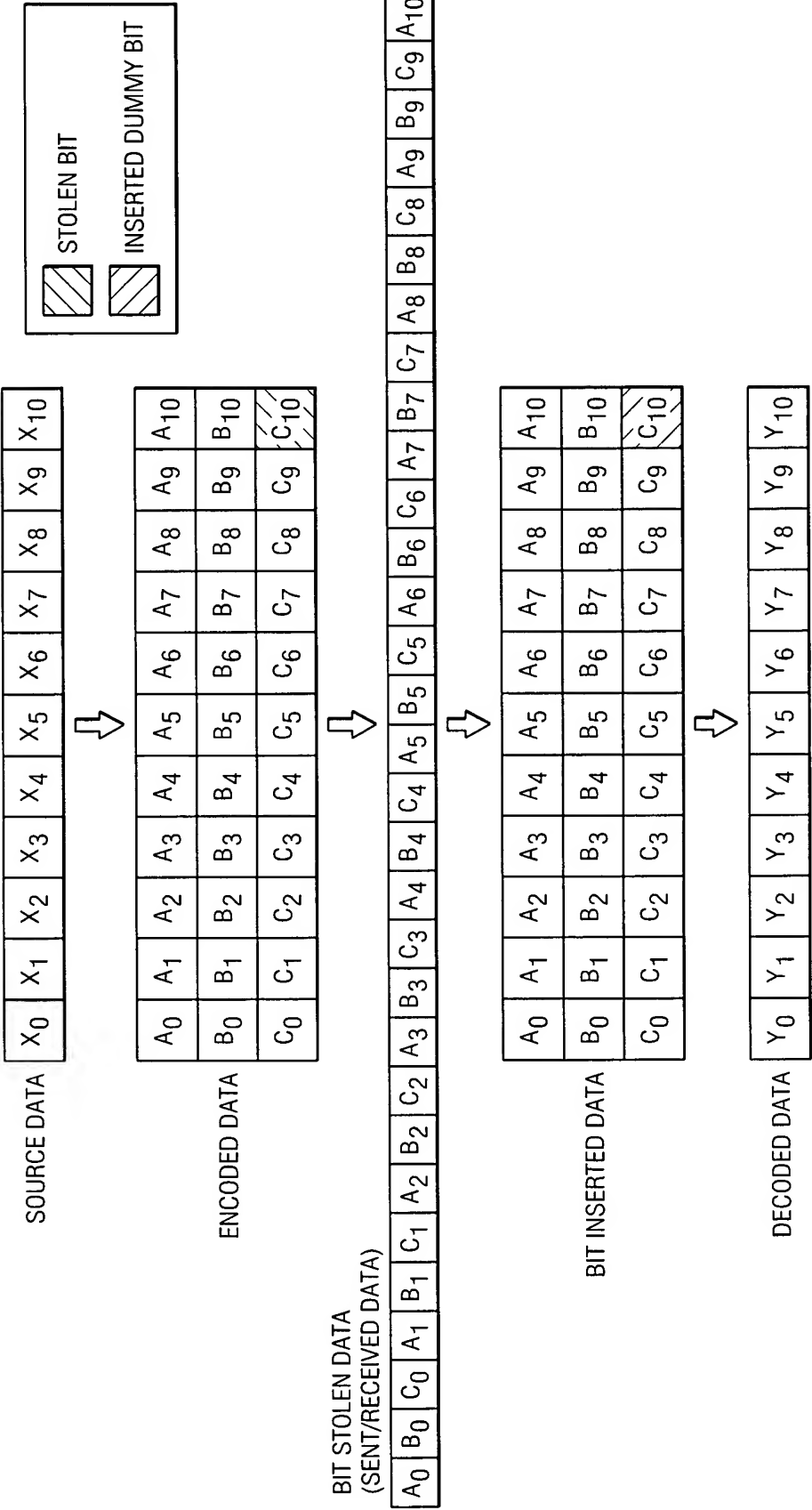


FIG. 16

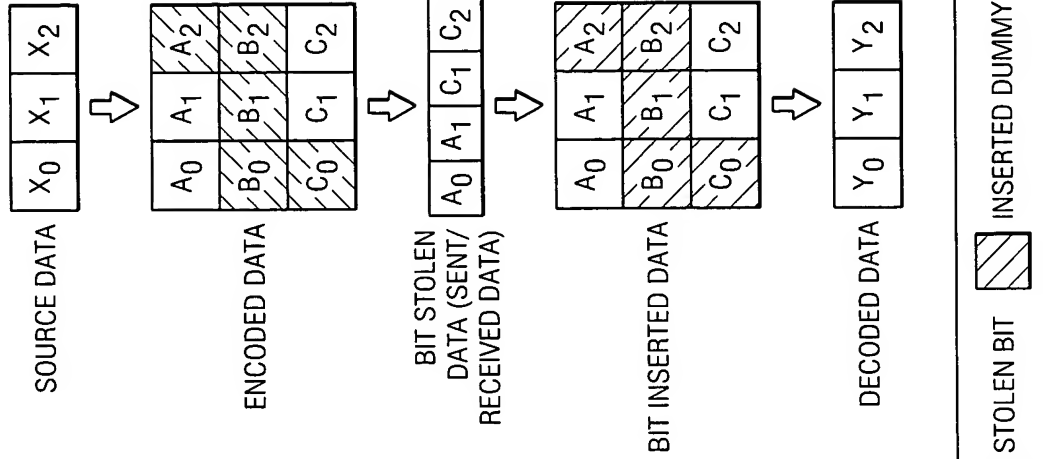
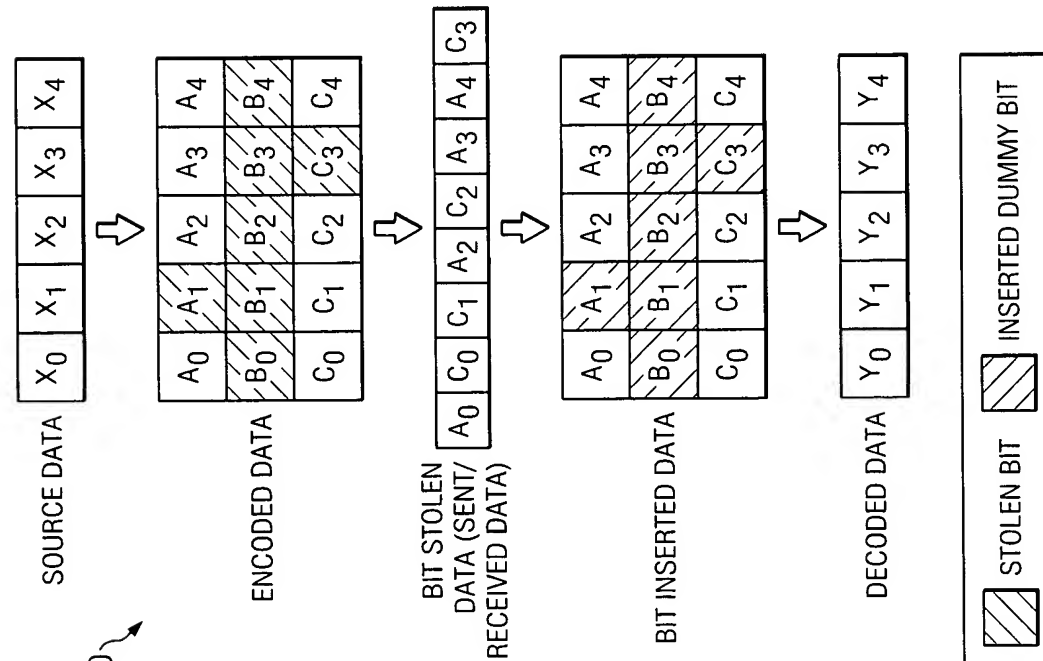
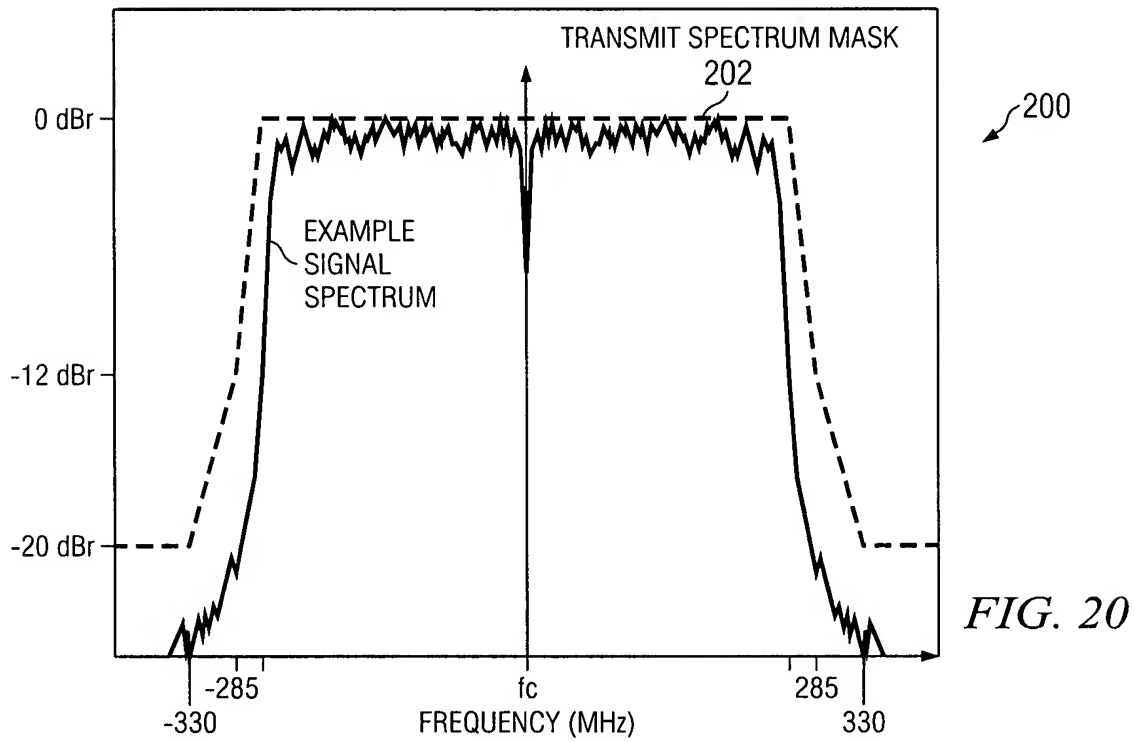
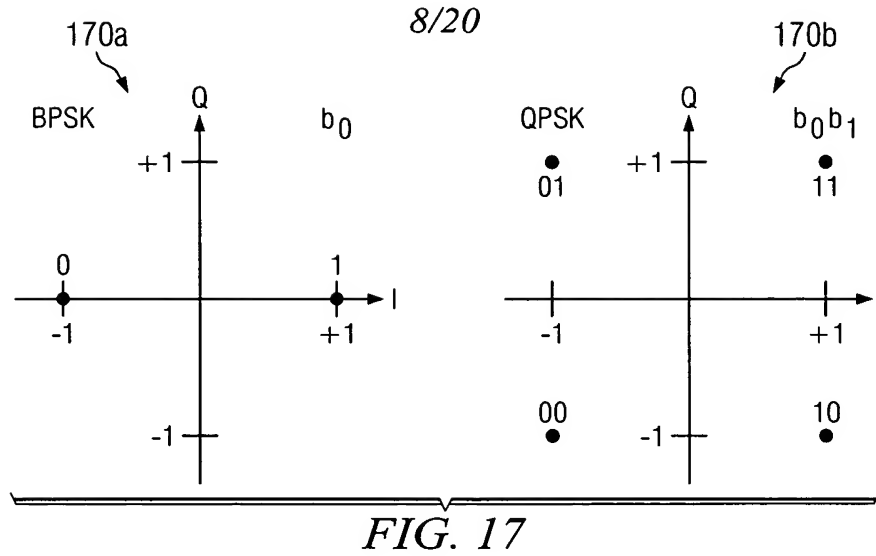


FIG. 15





$IS_1 = \{\text{Channel_2, Channel_1, Channel_3, Channel_1, Channel_2, Channel_3, Repeats}\}$
 $IS_2 = \{\text{Channel_1, Channel_3, Channel_2, Channel_3, Channel_2, Channel_1, Repeats}\}$

FIG. 21

$RS_1 = \{IS_1, IS_3, IS_2, IS_1, IS_2, IS_3, \text{Repeats}\}$
 $RS_2 = \{IS_2, IS_3, IS_1, IS_3, IS_2, IS_1, \text{Repeats}\}$

FIG. 22

1. PRESET REGISTERS TO ALL ONES
2. SHIFT SERIAL DATA INPUT THROUGH SHIFT REGISTERS
3. TAKE ONES COMPLEMENT OF THE REMAINDER
4. TRANSMIT OUT SERIAL X^{15} FIRST

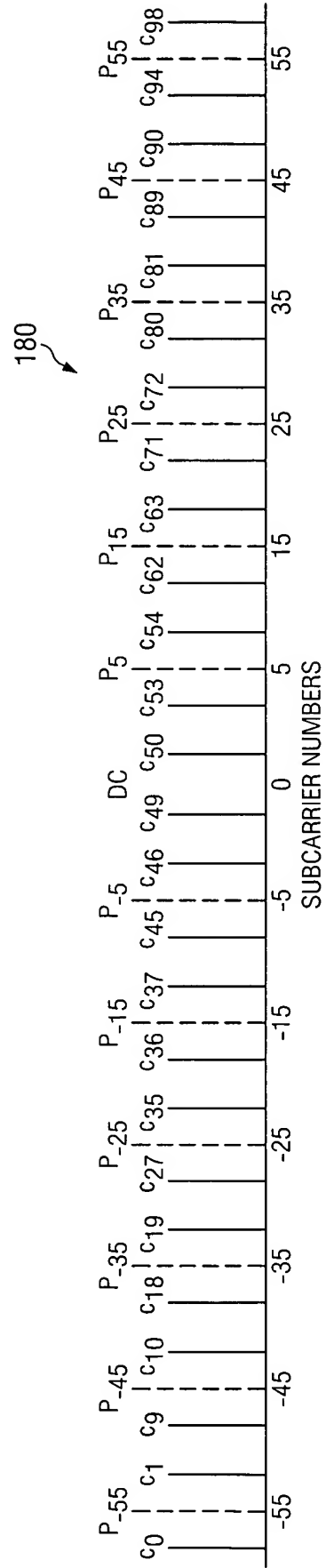
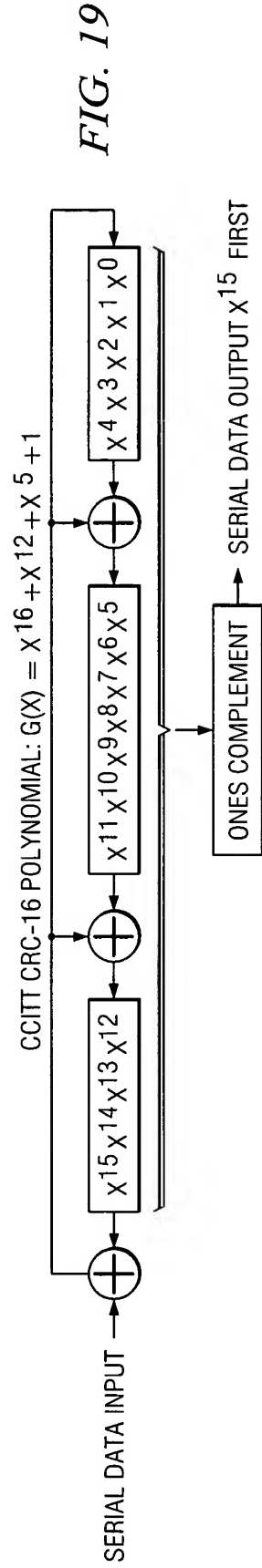
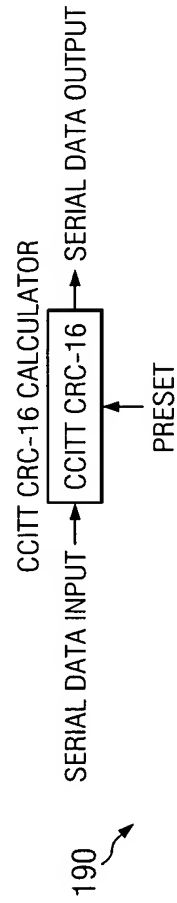


FIG. 18

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OCTETS: 1	1	1	1
ELEMENT ID	LENGTH	INTERLEAVING SEQUENCE	ROTATION SEQUENCE

FIG. 23

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234

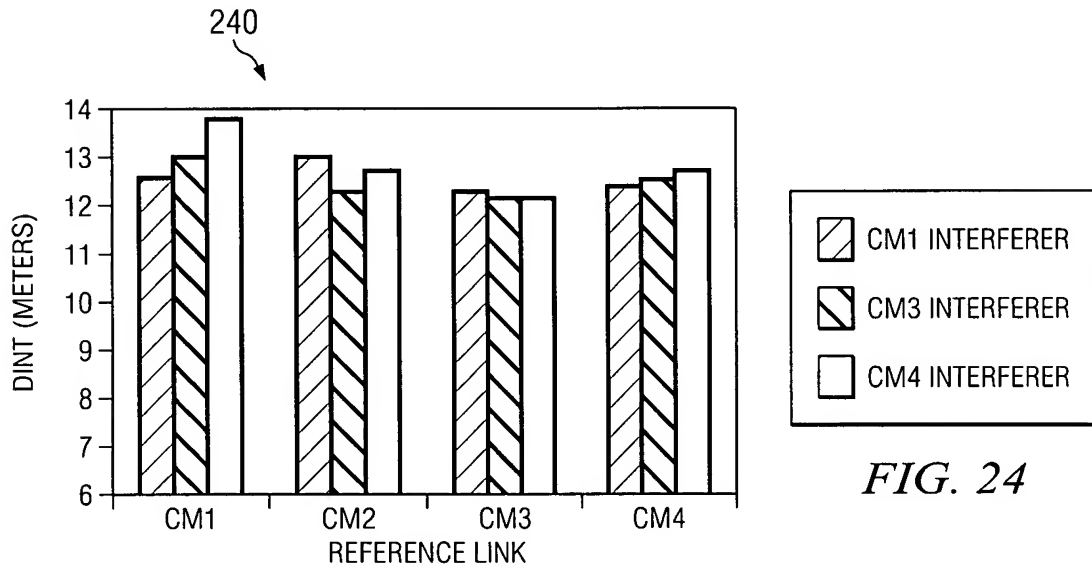


FIG. 24

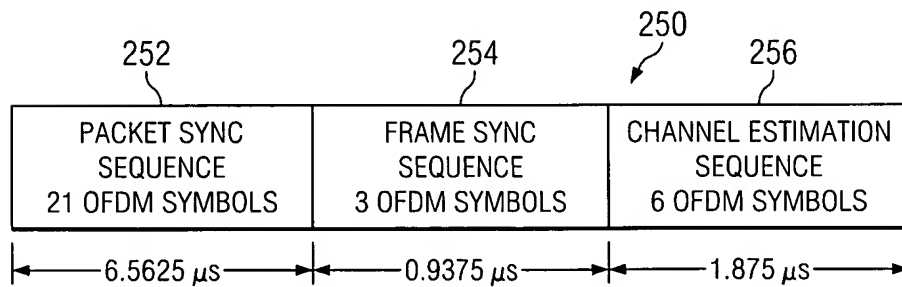


FIG. 25

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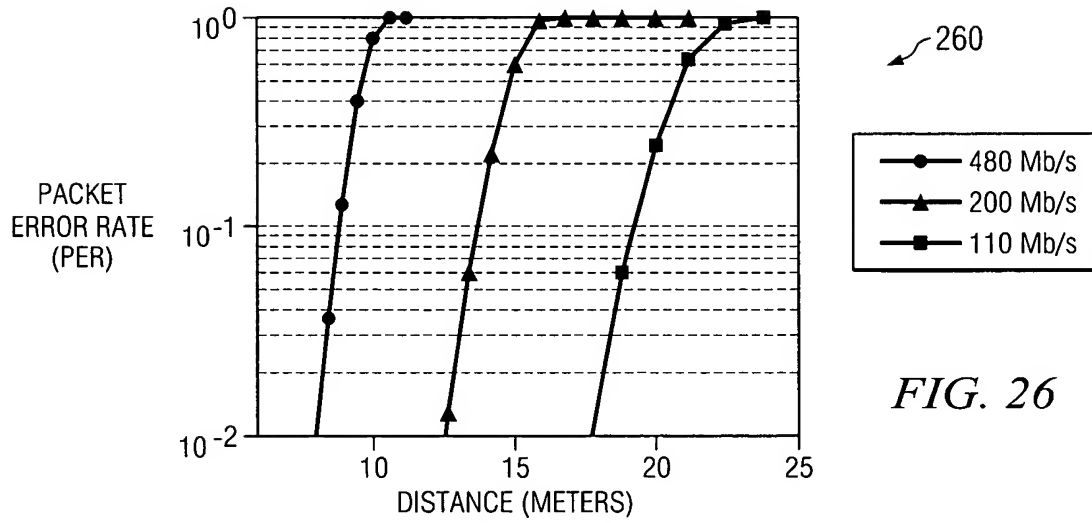


FIG. 26

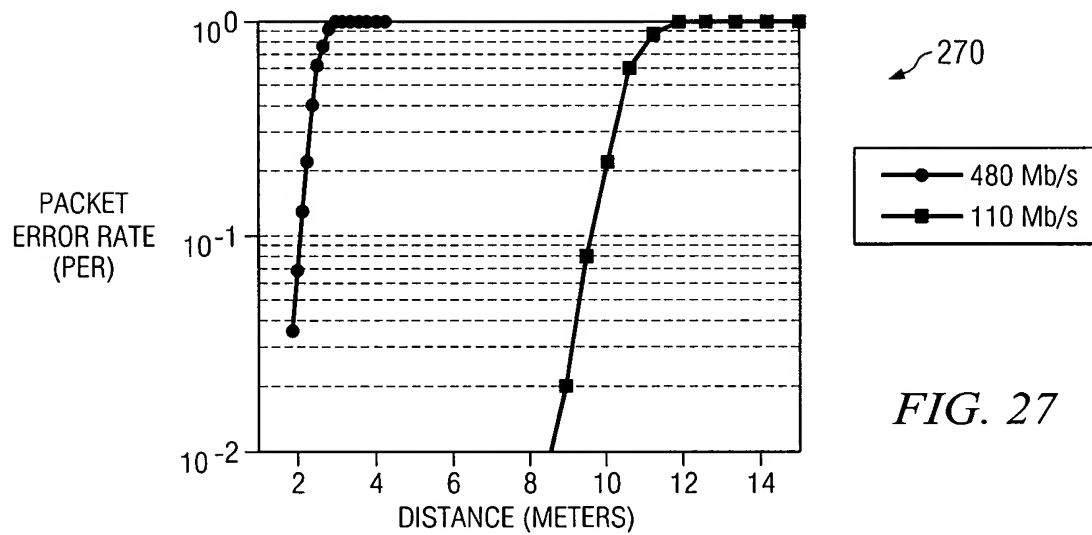


FIG. 27

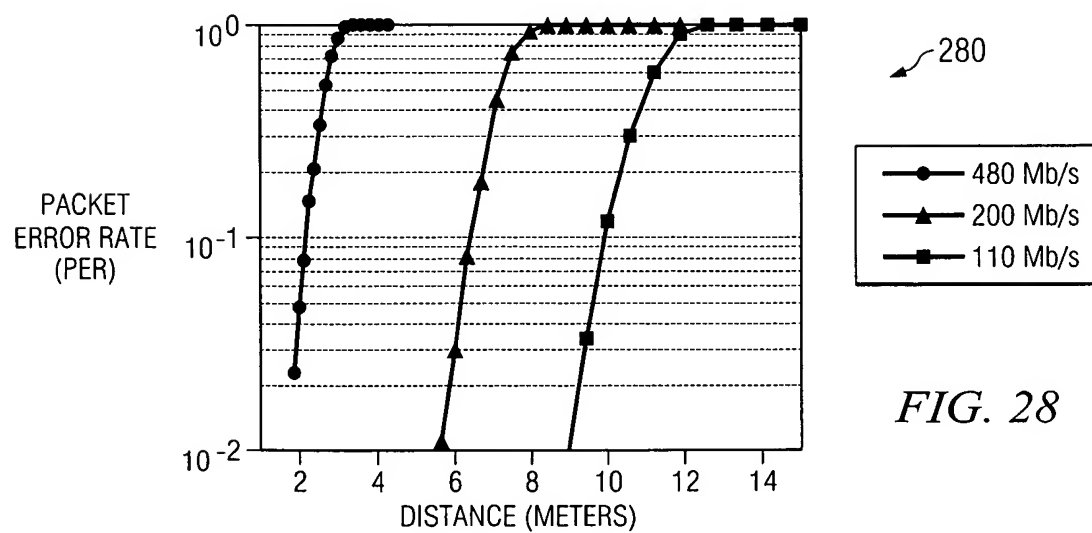


FIG. 28

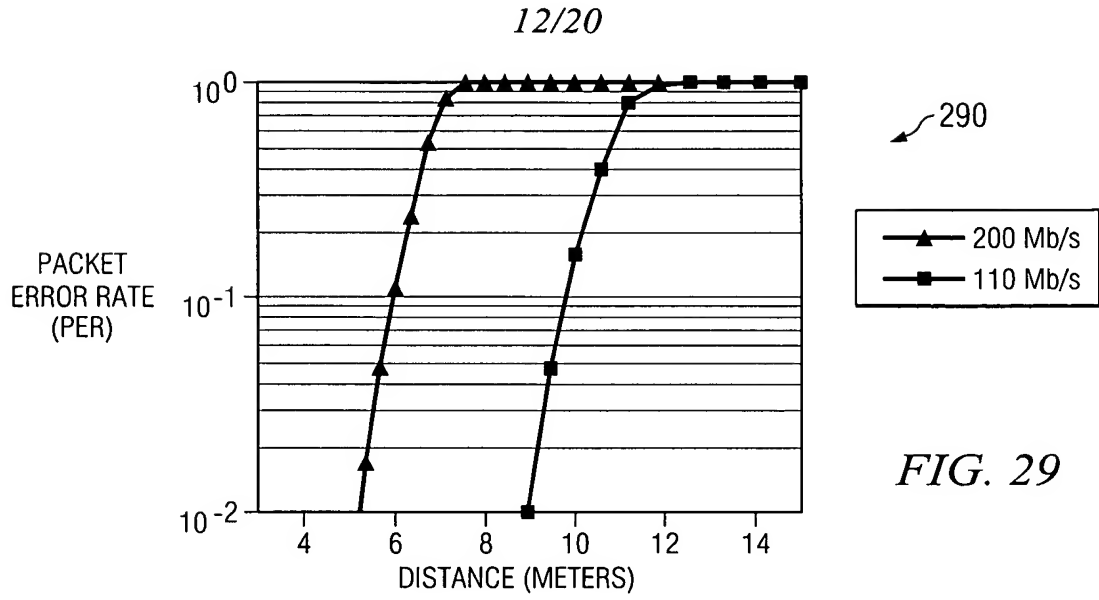


FIG. 29

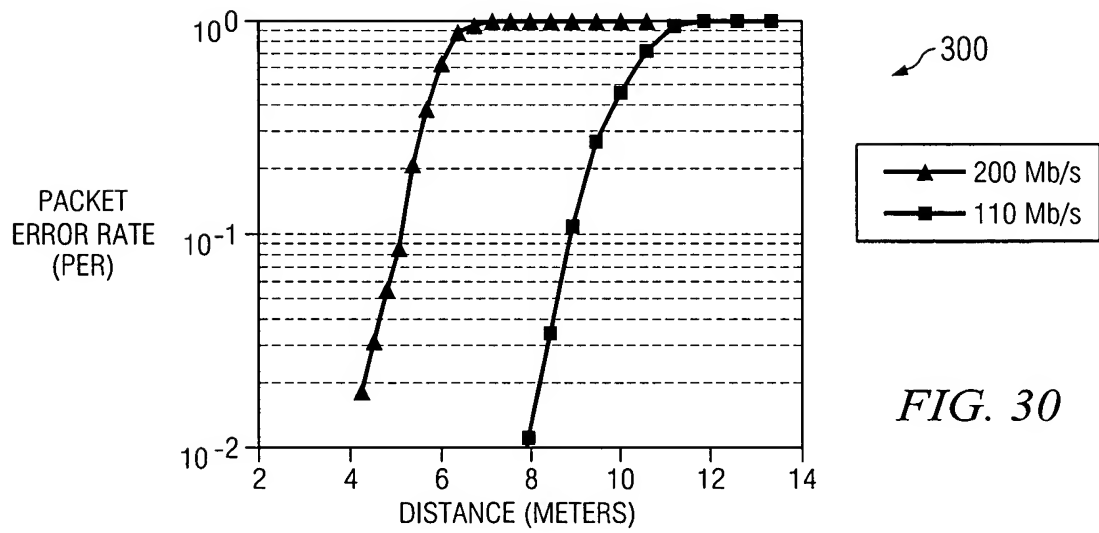


FIG. 30

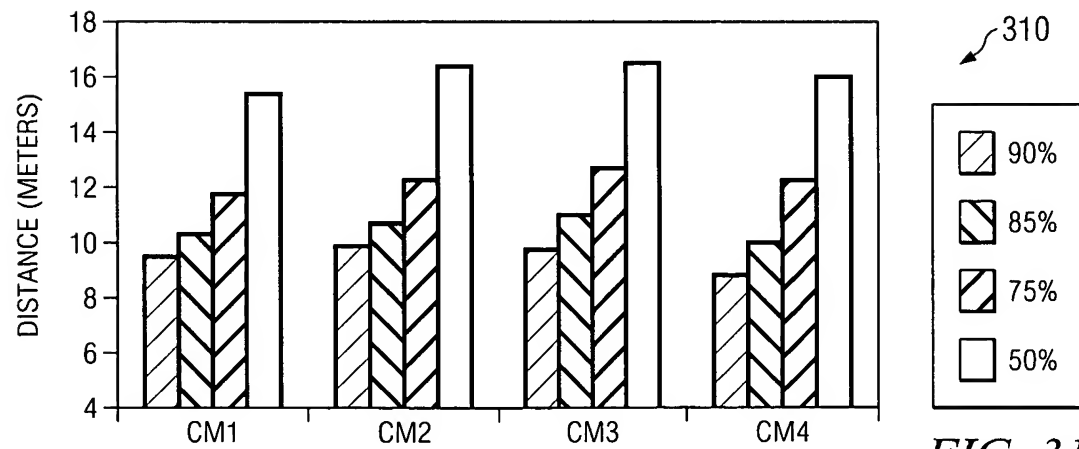


FIG. 31

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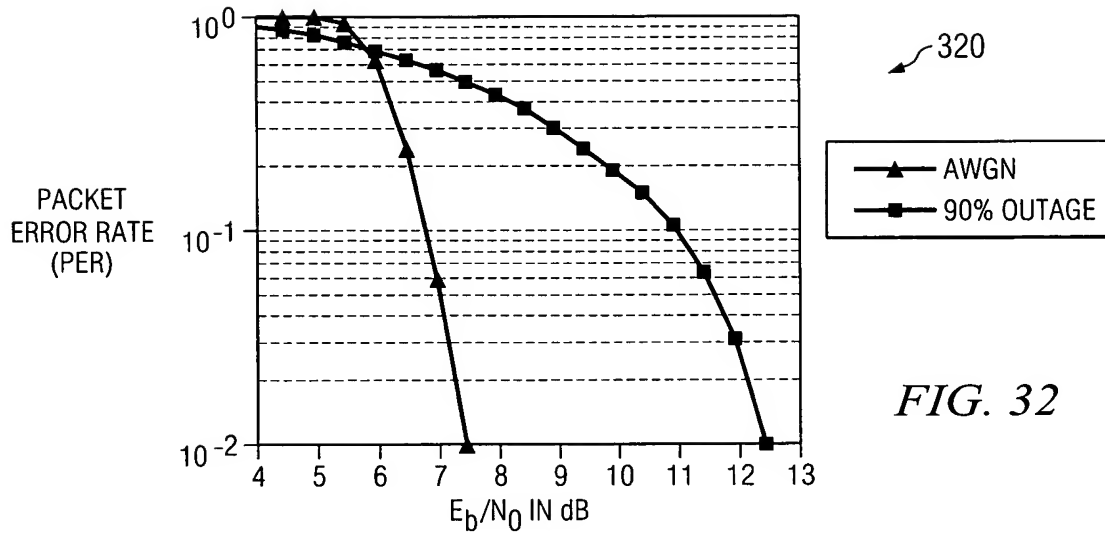


FIG. 32

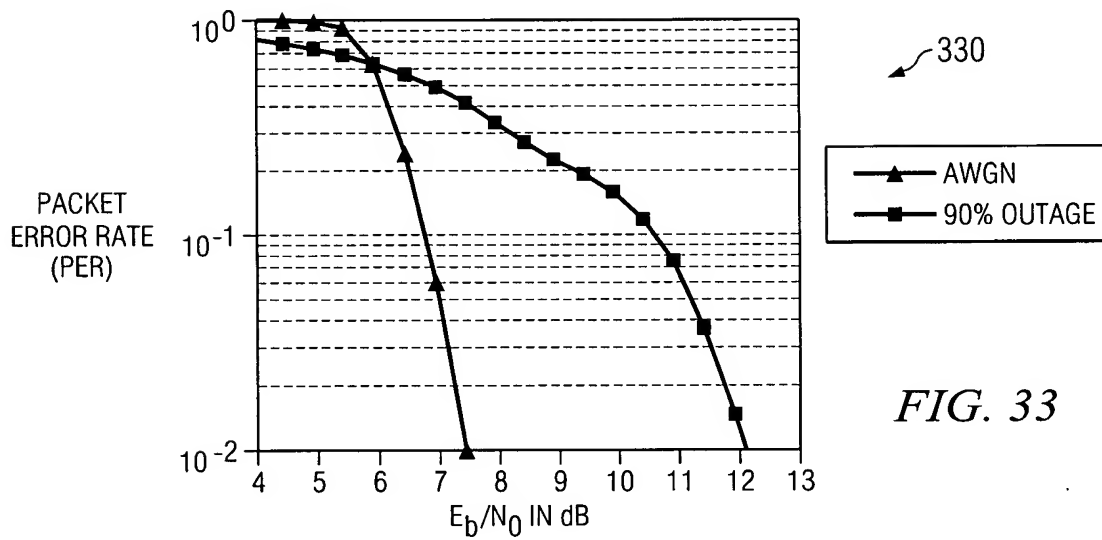


FIG. 33

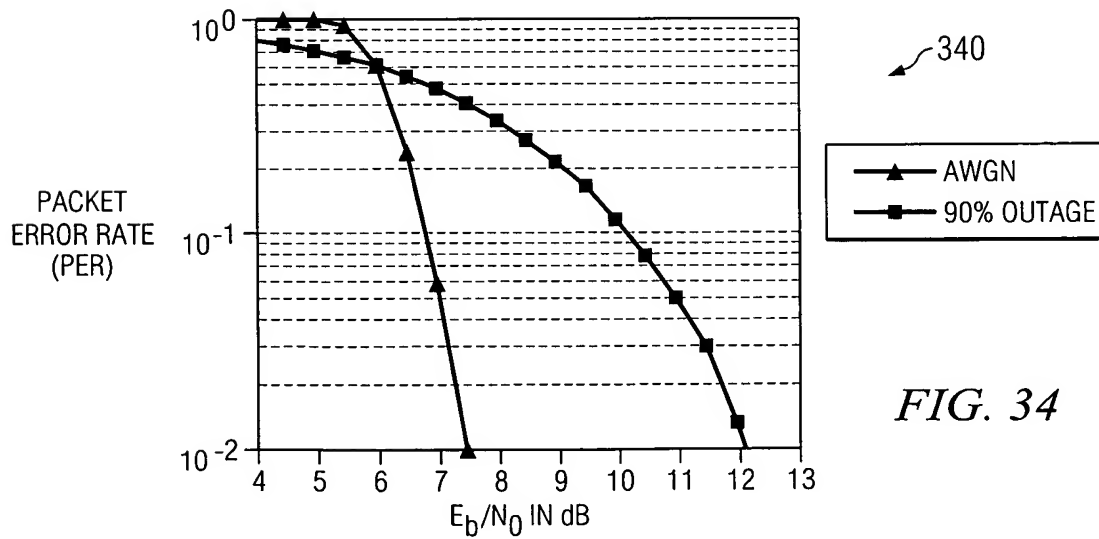
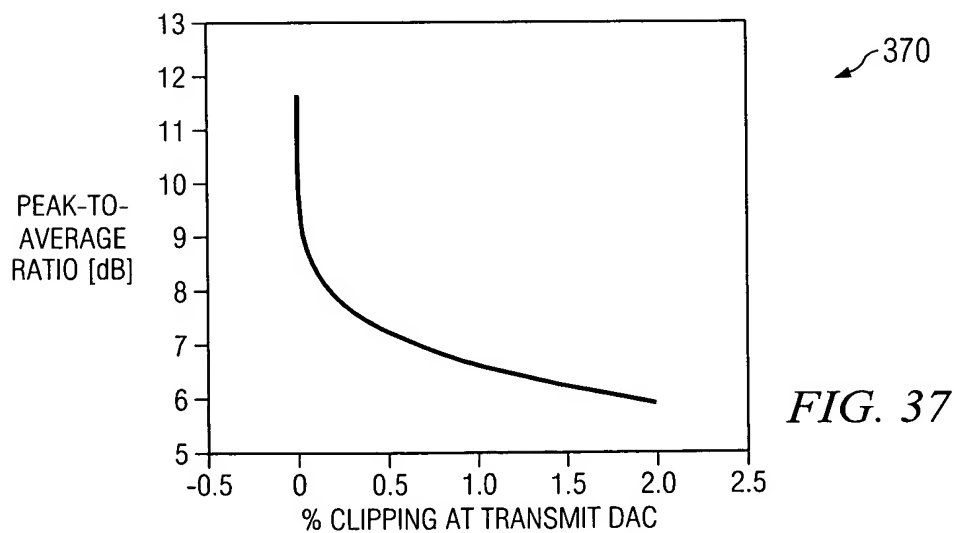
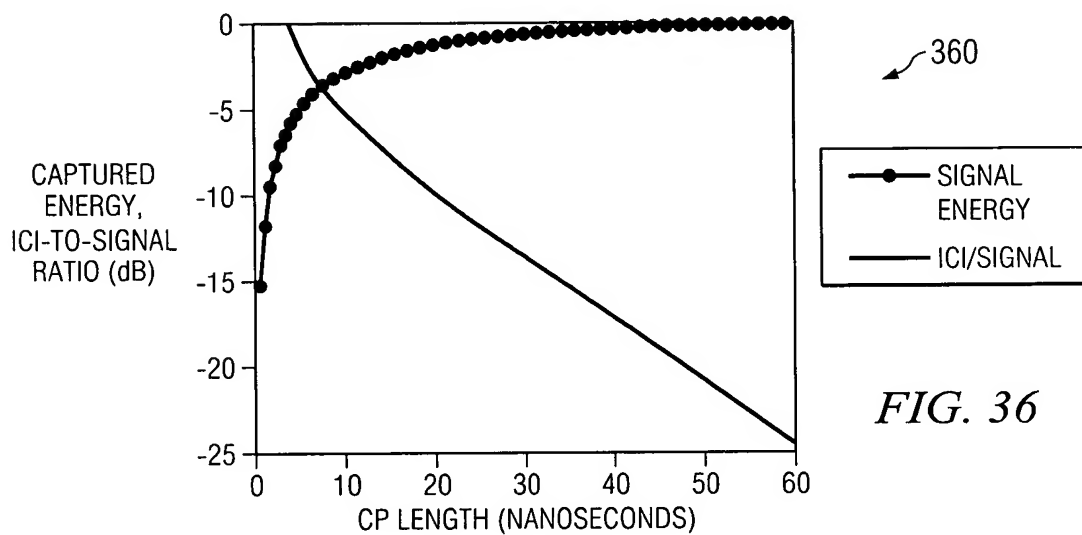
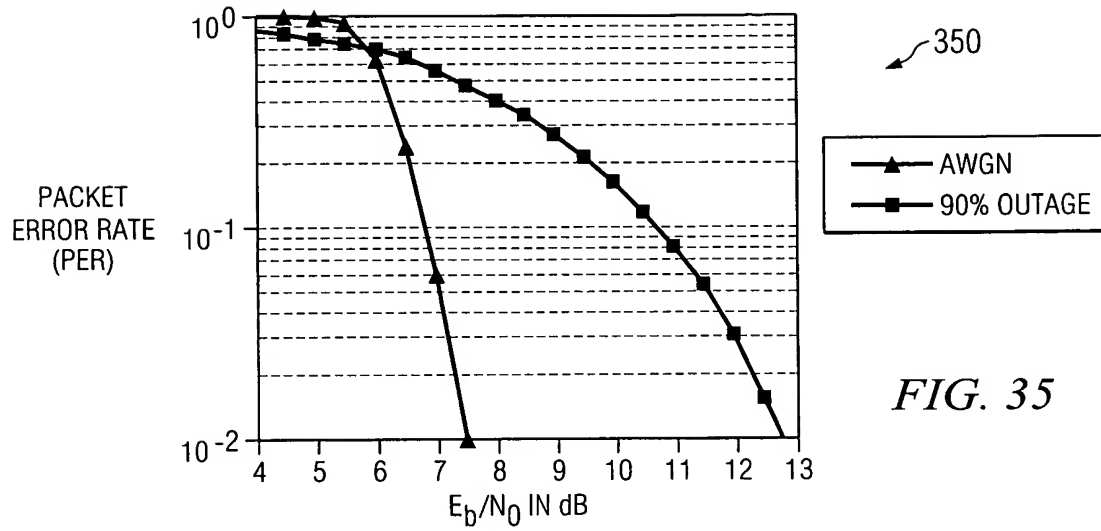
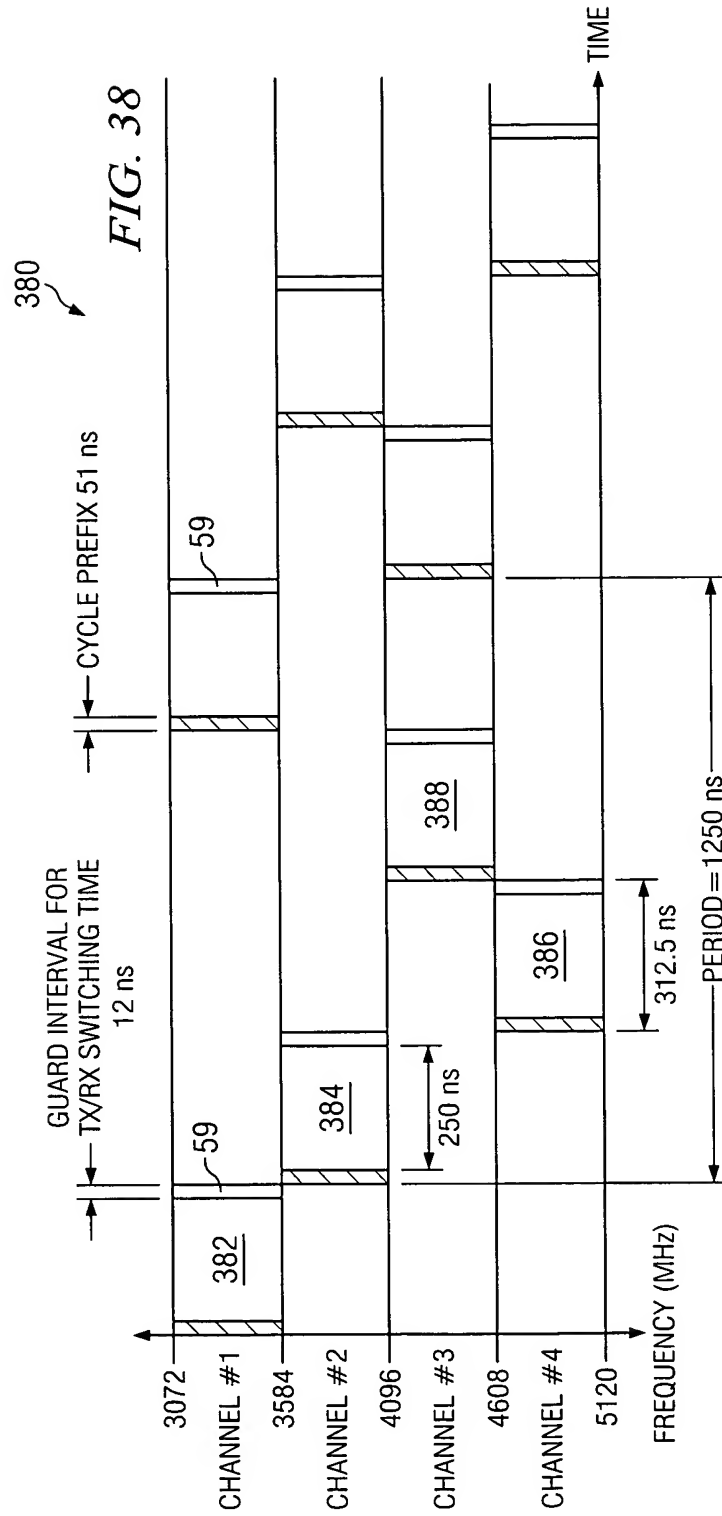
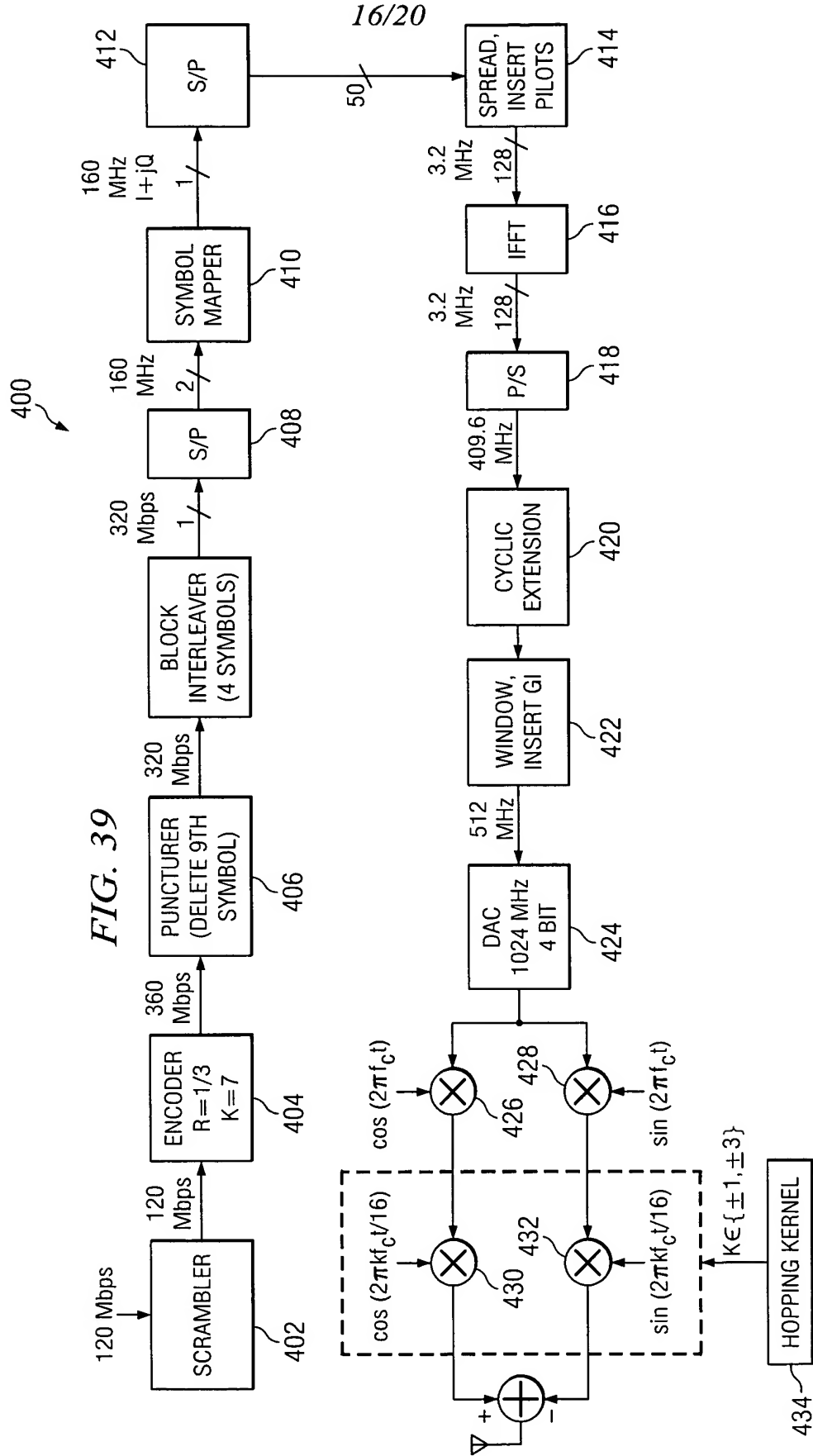


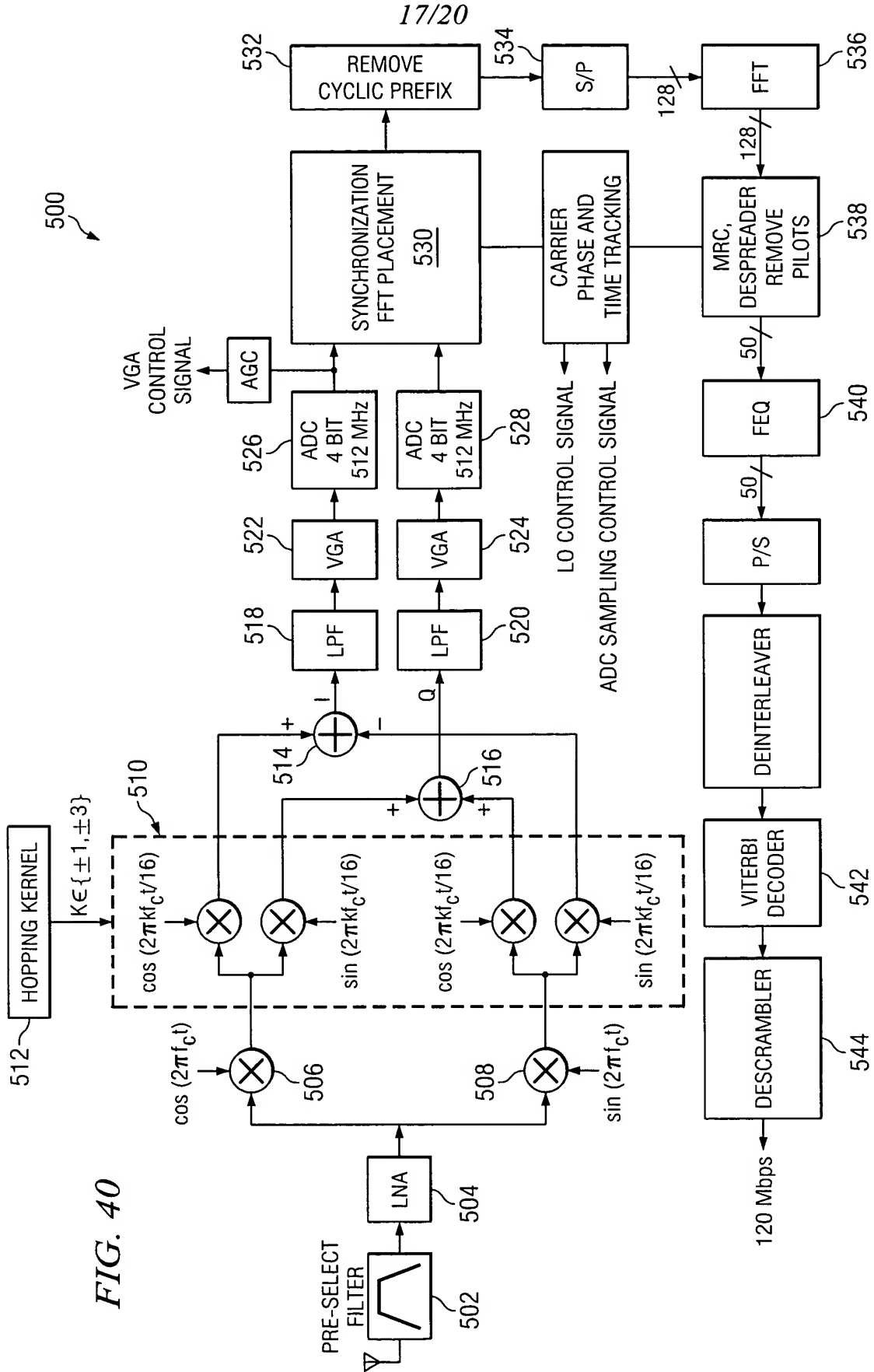
FIG. 34

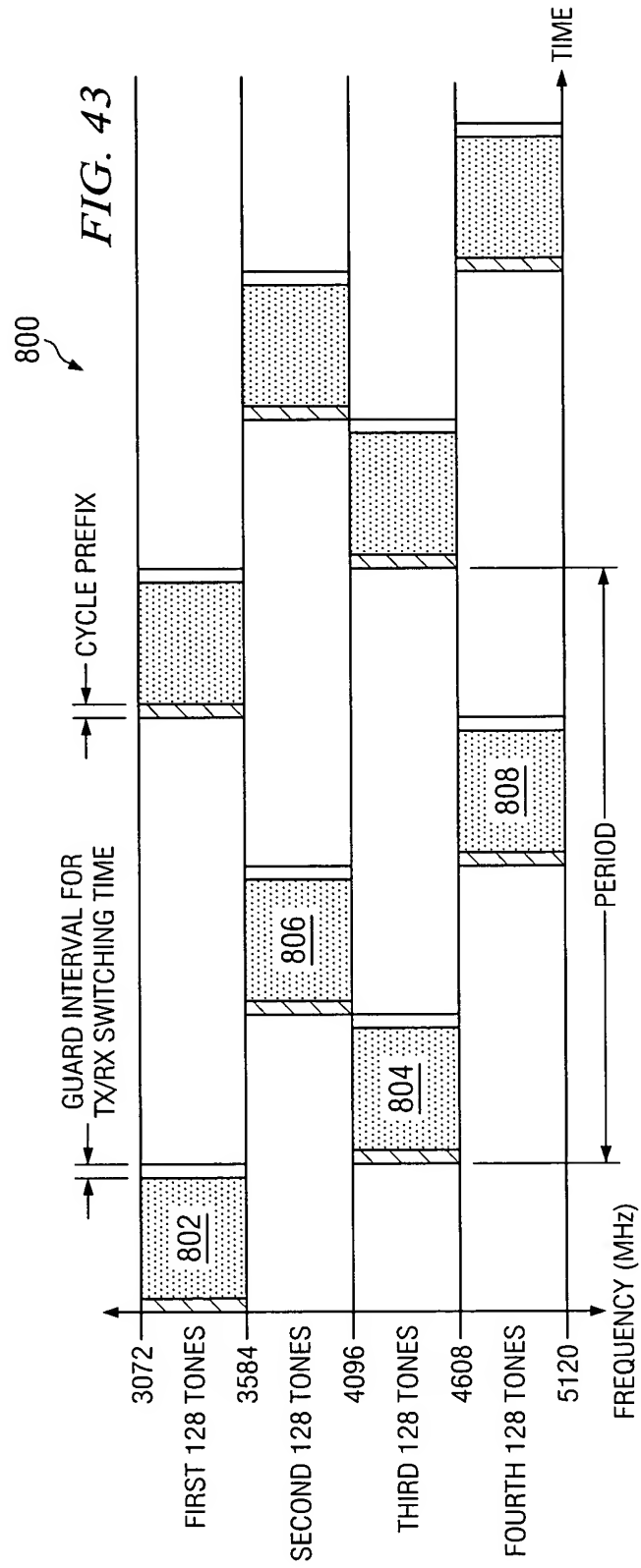
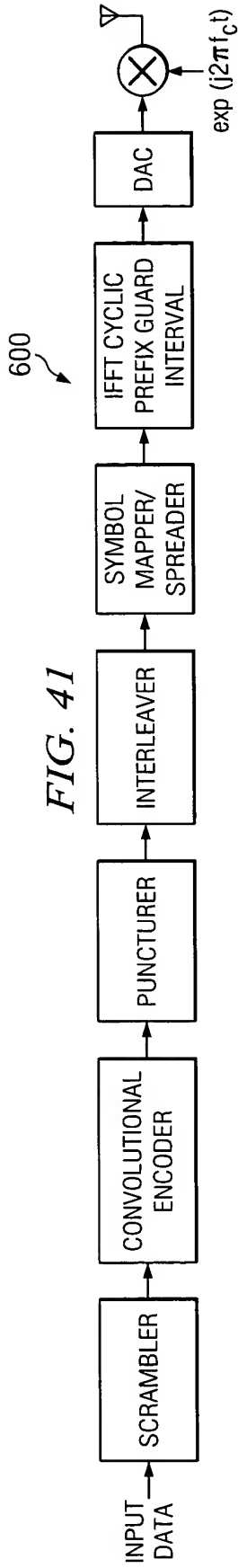
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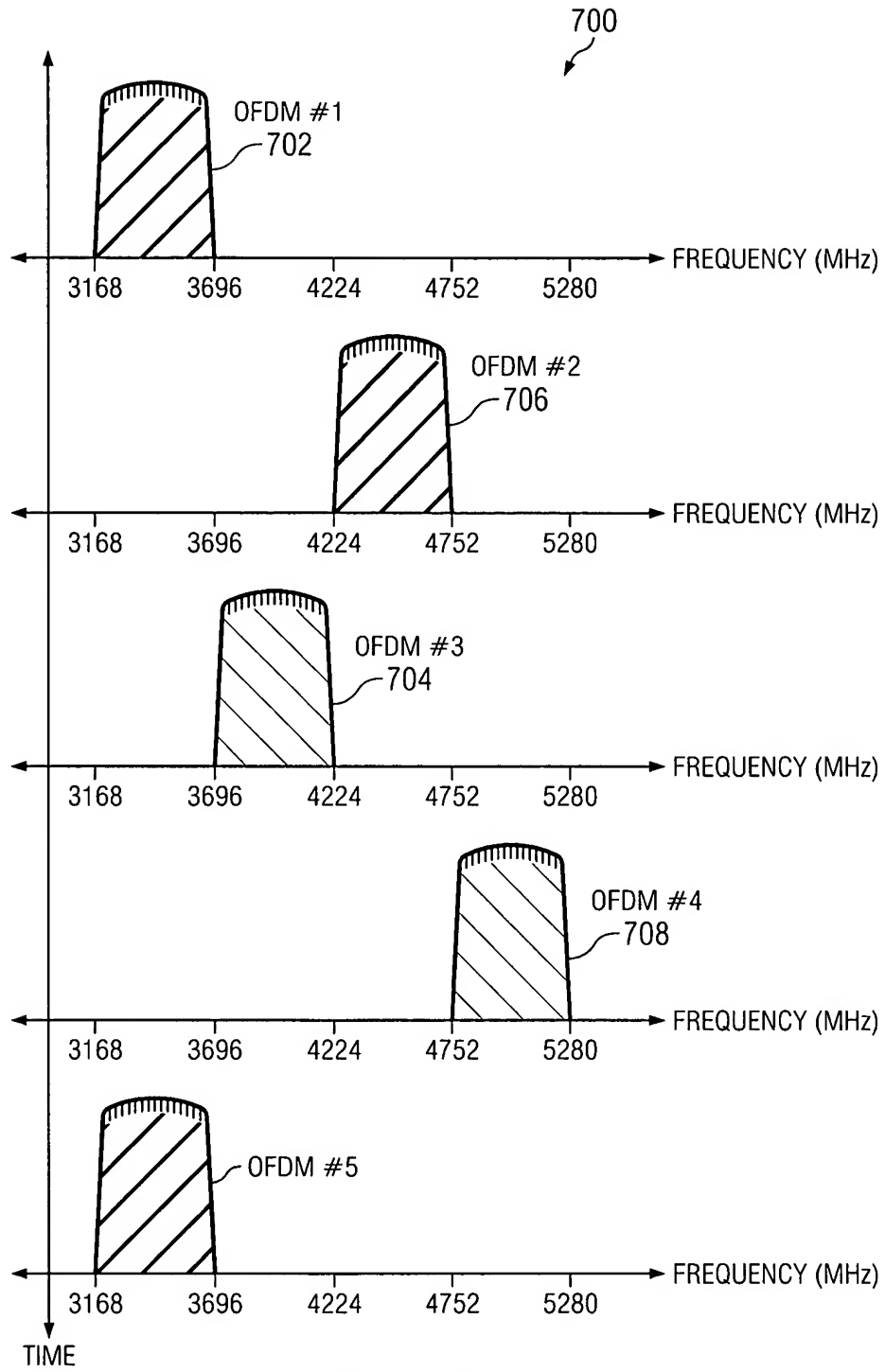


FIG. 42

